Supporting Quotes for the CopprLink™ Specification

Astera Labs

"Our Aries PCIe/CXL Smart Cable Modules™ enable robust and long reach copper cabling for rack-to-rack connectivity for GPUs, CPUs, and memory in disaggregated AI and cloud infrastructure. As active contributors to PCI-SIG® technology, we support efforts such as the new CopprLink™ Cable specifications to standardize PCIe® cabling for a variety of applications including internal cables, external intra-rack cables, and external inter-rack cables."

~ Casey Morrison, Chief Product Officer and Co-founder, Astera Labs

Lightelligence

"Lightelligence is a proud supporter of the recent CopprLink™ specification update for PCIe® 5.0 and 6.0 technology, which adds enhancements for active optical cables (AOCs) to join the PCIe specification ecosystem."

~ Ron Swartzentruber, Director of Engineering, Lightelligence

Molex

"Molex is thrilled to introduce our CopprLink™ cables to the PCI-SIG community. The CopprLink External form factor is a high-density interconnector for PCIe® external cabling capable of delivering 16 lanes of data rates exceeding 64 Gbps per lane. Our collaboration with member companies will enable the ecosystem to accelerate the deployment of the PCIe 5.0/6.0 specifications to meet increasing demand for next-generation data centers and high-performance computing."

~ Scott Sommers, Director of Standards, Datacom and Specialty Solutions, Molex

Samtec

"Rapid changes in artificial intelligence and accelerated computing architectures necessitate the signal reach and architectural flexibility twinax copper cabling provides. The new CopprLink™ 5.0/6.0 internal and external cabling specifications from PCI-SIG® enable 32 GT/s and 64 GT/s bandwidth cabling options for extensible system architectures around the data center."

~ Matthew Burns, Global Director, Technical Market, Samtec