



## Supporting Quotes for the PCI-SIG® 1,000 Members Announcement

### **Arm**

"Community-defined standards are integral to enabling the industrywide innovation needed to meet the demanding workloads of AI, ML, and HPC. This significant milestone demonstrates PCI-SIG's commitment to supporting a diverse computing ecosystem, and we look forward to seeing the benefits of the PCIe 7.0 specification on Arm-based systems."

~ *Dong Wei, Standards Architect and Fellow, Arm*

### **Astera Labs**

"Astera Labs is a proud member of PCI-SIG, contributing to the development of PCIe technology which serves as the backbone of innovation for AI and cloud infrastructure. We congratulate PCI-SIG on reaching 1,000 members and look forward to continued collaboration in shaping the future of connectivity."

~ *Casey Morrison, Chief Product Officer, Astera Labs*

### **Astron Connectivity**

"1,000 members! PCI-SIG has built more than just standards—it's built a community that drives progress and inspires innovation. Here's to continuing this shared journey and achieving even greater heights together!"

~ *TJ Lin, General Manager, Astron Connectivity*

### **Granite River Labs**

"As the first PCI Express (PCIe) Authorized Test Lab approved by the PCI-SIG to provide official Integrators List Compliance Testing, Granite River Labs (GRL) is proud to contribute to the growth and adoption of PCIe technology, the scalable and cost-effective solution for data-intensive applications like AI/ML, automotive, and data centers. The rigorous PCIe compliance testing program provides a critical step towards assuring end users that the product is interoperable and backwards compatible with prior generations of PCIe architecture. With comprehensive testing capabilities and automated software test solutions spanning PCIe 6.0 and earlier



specifications, GRL is committed to supporting the continued growth and success of the PCIe ecosystem."

*~ Eugene Sushansky, Executive Vice President of Global Engineering, Granite River Labs*

## **Intel**

"We congratulate PCI-SIG on reaching the significant milestone of 1,000 members. At Intel, we are strong advocates for open standards, and as one of the initial companies driving PCI-SIG, we are thrilled to witness its continued innovation over the past three decades. PCI Express technology has truly become the universal I/O interconnect, powering a wide range of computing segments, and we look forward to its ongoing success."

*~ Dr. Debendra Das Sharma, Senior Fellow, Intel*

## **Microchip**

"Microchip congratulates PCI-SIG on this momentous milestone. As a member company, under the original monicker of PMC-Sierra, and later to Microsemi and now Microchip, we have long enjoyed the technical corroboration and technical expertise provided by the organization and its continued evolution."

*~ Michael Chessin, Principal Engineer-Design, Microchip*

## **Prodigy Technovations**

"At Prodigy Technovations, we value our membership with PCI-SIG as it fosters collaboration and innovation in the PCIe technology ecosystem. The advancement of PCIe technology continues to reshape industries by meeting the demand for faster, more efficient data transfer, and we are proud to contribute to and benefit from this transformative journey."

*~ Godfree Coelho, Founder & CEO, Prodigy Technovations*

## **Synopsys**

"This milestone highlights the vital role PCI-SIG plays in fostering a collaborative ecosystem to advance high-performance connectivity. Synopsys has been a



provider of PHY, controller and verification IP for PCIe technology and has participated in the PCI-SIG standards body for more than 20 years. We are focused on continuing our contributions to future generations of PCI Express technology."

*~ Neeraj Paliwal, Sr. Vice President, IP Product Management, Synopsys*

### **VA Linux Systems Japan**

"VA Linux Systems Japan is pleased to be a member of PCI-SIG, and we would like to express our deep respect for the achievements and valuable contributions of all PCI-SIG members. As PCIe architecture becomes the essential backbone for interconnectivity in distributed systems, we are committed to supporting the growth and innovation of the PCIe ecosystem."

*~ Teruyoshi Hazama, President and CEO, VA Linux Systems Japan*