



## PCI-SIG® Compliance Workshop #118

December 6 – December 10, 2021

Embassy Suites Burlingame  
Burlingame, California

**Please read carefully for registration information and the details.**

**Please come prepared.**

### **When**

The PCI-SIG Compliance Workshop #118 will be held December 6 – December 10, 2021. The registration deadline is **5pm (PT) on Thursday, November 11, 2021. The Hotel Reservation deadline is Monday, November 22, 2021. Hotel rooms under the discounted rate are on a first come, first serve basis.**

### **Objective**

**PCI:** Compliance Workshops are held to promote PCI Express® specification compliance in the industry with the goals of eliminating interoperability issues and ensuring proper implementation of PCI specifications.

**Member:** Participation provides an opportunity to find and fix problems before release. This saves your company time and resources while offering valuable networking and training opportunities with your fellow engineers.

Products may be added to the PCI-SIG Integrators List by passing the testing requirements at a compliance workshop **and** submitting a PCI-SIG Integrators List [Product Listing Request Form](#).

### **How**

PCI-SIG schedules compliance test sessions, plus private test sessions between Add-in Card and System/Systemboard/BIOS vendors for testing. PCI-SIG also provides technical consultants to assist in testing and fault analysis. **Engineers must accompany all equipment tested.**

### **What Technologies Will Be Tested**

PCI Express 4.0  
PCI Express 3.0  
PCI Express U.2@/SFF-8639/ (8 GT/s)

**PCI Express 4.0 Official Testing:** The PCI-SIG will be offering official testing for PCIe 4.0 at 16 GT/s for both add-in cards and systems. You must provide a separate device and attendee to conduct the PCIe 4.0 testing for each product you register.

- **Lane Margining:** please be aware that the Lane Margining test that was conducted at previous FYI workshops will remain an FYI test for 4.0 Integrators List testing.
- **Please note the following component policy for 4.0 official Integrators List testing:** PCI-SIG Integrators List testing for CEM devices is performed at the PCI Express connector, and AICs or System boards that pass can be added to the Integrators List. Vendors that wish to place the corresponding component onto the Integrators List based on that passing result, must certify to PCI-SIG that the electrical interface exposed at the CEM connector is the electrical interface of that component. Note that devices such as redrivers or retimers when present between the component and the CEM connector prevent the full electrical and functional testing of the component itself, and therefore while such an AIC or System board may achieve a passing result (and be placed on the Integrators List as an AIC or System), the component itself has not achieved a passing result and therefore cannot be placed on the Integrators List as a Component unless specifically tested.

**PCI Express 4.0 Preliminary FYI Testing for Retimers:** PCI-SIG will also offer a preliminary FYI PCIe 4.0 Retimer test program. The preliminary FYI PCIe 4.0 Retimer testing is planned to include the PCI Express Base Specification 4.0 electrical testing. The Retimer vendor must provide a short channel silicon evaluation board and necessary adapters to SMA test equipment connections.

**PCI Express U.2/SFF-8639 (8 GT/s) Testing:** Please note that as of Compliance Workshop #106 PCI-SIG is offering official SFF-8639/U.2 testing for both Add-in Cards and Systems. Testing can qualify a device for placement on the Integrators List. If you'd like your device listed as **both** CEM and U.2, on the Integrators List, please ensure that you

register \*TWO\* instances of your device – one for the SFF-8639/ PCI Express U.2 testing, and one for the CEM form factor (bringing your own adapter for CEM).

SFF-8639/PCI Express U.2 Cards must be registered separately from CEM based Cards. Please note that one person cannot escort both SFF-8639/PCI Express U.2 Cards and CEM based cards, you will need to register an additional attendee to manage the additional testing schedule. Please see the registration restrictions in the “How to Register” section.

### **Cost**

Attendance at this **Members Only** event is free. Please note that your credit card information will be collected for product registration(s); however, you will **not** be charged unless you do not bring your product to the event or your product registration is not cancelled by **5 PM PDT on Thursday, November 11, 2021**.

This event is non-hosted. Participants pay for their own travel, food, and hotel expenses. Lunch is provided for attendees Tuesday through Friday.

### **Where**

Embassy Suites Burlingame – San Francisco Airport Hotel  
150 Anza Blvd.  
Burlingame, CA 94010

### **Shipping Instructions**

Please ensure your shipment does not arrive at the hotel prior to **Thursday, December 2, 2021**. If you would like to ship items to the hotel, **please ensure you complete the [group package delivery form](#) and send to Kristin Basmajian using the contact information provided on the form.**

**Ship to:** Embassy Suites Burlingame – San Francisco Airport Waterfront, 150 Anza Blvd., Burlingame, CA 94010 USA  
**Tel:** 650-342-4600, Fax: 650-343-8137  
**Attn:** Hold for (Your name / Your company name / Tel.) / December 6 –10, 2021 PCI-SIG / A. Foster

### **Hotel Reservations – Reserve your room early!**

\*Reservations cut-off date is Monday, **November 22, 2021**. To ensure the group rate is applied and the hotel has rooms available please make your reservations as soon as possible. Hotel rooms under the discounted rate are on a first come, first serve basis..

When making a hotel reservation, please use our group specific page:

<https://www.hilton.com/en/book/reservation/deeplink/?ctyhocn=SFOBGES&groupCode=CESPCI&arrivaldate=2021-12-05&departuredate=2021-12-11&cid=OM,BW,HILTONLINK,EN,DirectLink&fromId=HILTONLINKDIRECT>

## **HOW TO REGISTER**

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### **I. EVENT REGISTRATION**

- All Attendees **MUST** accompany a co-worker who is registered to test equipment.
- **On-Line Registration** – <https://pcisig.com/events/compliance-workshops>.
- Online registration ends at **5:00pm Pacific Time on Thursday, November 11, 2021**.

### **II. SLEEPING ROOM RESERVATIONS**

- Instructions are found above.
- **NOTE: A testing room must be reserved by the System vendor company to participate. The room reservation must check in on Monday, December 6<sup>th</sup> with a checkout of Friday, December 10<sup>th</sup>.**

### **III. ON-SITE CHECK-IN**

- Name badges and test schedules will be distributed on the first day of the event from 8:00-8:45am (subject to change) outside the PCI-SIG Hospitality Suite. An all attendee email will be sent if schedule and name badge pick up time has changed.

# PCI-SIG Compliance Workshop Test Syllabus

## One-on-One Test Sessions

PCI-SIG Compliance Workshops include one-on-one private 15 minute to one-hour test sessions between an Add-in Card vendor and a System vendor. System vendors and Add-in Card vendors will test their products in as many combinations as time and capabilities permit. The results of these test sessions are recorded and will be used as one of the elements in determining PCI-SIG compliance.

The one-on-one test sessions are held in hotel suites as reserved by the System/System board/BIOS vendors (Add-in Card vendors travel to the system vendor suites). The privacy of these sessions promotes willingness to test products that may not be fully "market ready." Additionally, vendors are able to establish contacts which enable a strong PCI Engineering community and encourage future cooperation for debugging and development.

### Test Details:

1. Install Add-in Card to be tested and attach peripheral (as appropriate).
2. Boot OS.
3. Run Application(s) to demonstrate detection & functional operation, file transfers for disk and LAN adapters, and graphics application for graphics adapters (note that demonstrating only detection and configuration is not adequate, as demonstrating functional operation is also required).

## Add-In Card Test Session

PCI-SIG hosts staffed compliance test suites to test PCI Add-in Cards using test software and hardware to provide extensive testing for compliance against specific aspects of the appropriate PCI-SIG specification. Add-in Card vendors will be scheduled to visit each applicable compliance test suite during the compliance one-on-one test sessions. Results will be recorded and used as one of the elements in determining PCI-SIG compliance.

### Test Details:

1. Remove (if necessary) system Add-in Card containing the same function that the Add-in Card being tested represents.
2. Insert vendor Add-in Card to be tested and attach the appropriate peripheral.
3. Load drivers (if needed) and the run tests.

## System/Systemboard/BIOS Test Sessions

PCI-SIG hosts staffed compliance test suites for testing System/Systemboard/BIOS products. Commercially available PCI-SIG test cards and test software serve as the test platform for extensive testing for specific aspects of the system environment. System vendors will need to roam to the PCI-SIG Gold Suite system compliance test sessions.

## Integrators List Policy

Products that pass 80% of one-on-one interoperability test sessions, pass all appropriate PCI-SIG compliance tests, **and** submit the [Product Listing Request Form](#), may be listed on a PCI-SIG Integrators List. The Integrators List is used by a number of PCI-SIG companies to make product purchase decisions. When submitting the [Product Listing Request Form](#), only one product will be listed per product tested at a PCI-SIG Compliance Workshop. A product may be submitted at any point following the workshop where the product was tested, there is no deadline.

## Questions?

If you have any questions, please contact PCI-SIG at Tel. (503) 619-0569 or Fax (503) 644-6708 or e-mail PCI-SIG Administration at [compliance@pcisig.com](mailto:compliance@pcisig.com).

Thank you,

Reen Presnell  
Executive Director  
PCI-SIG