



PCI-SIG® Compliance Workshop #108

December 4 – 7, 2018

Embassy Suites Burlingame
Burlingame, California

Please read carefully for registration information and the details.

Please come prepared.

When

The PCI-SIG Compliance Workshop #108 will be held December 4-7, 2018. The registration deadline is **12noon on November 9, 2018. The Hotel Reservation deadline is Monday, November 19, 2018.**

Objective

PCI: Compliance Workshops are held to promote PCI Express® specification compliance in the industry with the goals of eliminating interoperability issues and ensuring proper implementation of PCI specifications.

Member: Participation provides an opportunity to find and fix problems before release. This saves your company time and resources while offering valuable networking and training opportunities with your fellow engineers.

Products may be added to the PCI-SIG Integrators List by passing the testing requirements at a compliance workshop **and** submitting a PCI-SIG Integrators List [Product Listing Request Form](#).

How

PCI-SIG schedules compliance test sessions, plus private test sessions between Add-in Card and System/Systemboard/BIOS vendors for testing. PCI-SIG also provides technical consultants to assist in testing and fault analysis. **Engineers must accompany all equipment tested.**

What Technologies Will Be Tested

PCI Express 4.0 (*FYI Only*)

PCI Express 3.0

PCI Express U.2/SFF-8639/ (8 GT/s)

PCI Express M.2 (8 GT/s) (*Preliminary FYI Only*)

PCI Express 4.0 Preliminary Testing: The PCI-SIG will be offering FYI testing for PCIe 4.0 at 16 GT/s for both add-in cards and systems. We will offer an FYI PCIe 4.0 retimer test program. FYI testing does not qualify a device for placement on the Integrators List. The FYI PCIe 4.0 testing is only for devices that support 16 GT/s and you must have a device with 16 GT/s functionality to register for and attend this testing. The FYI PCIe 4.0 retimer testing is planned to include the PCI Express Base Specification 4.0 electrical testing. The retimer vendor must provide a short channel silicon evaluation board and necessary adapters to SMA test equipment connections. The FYI PCIe 4.0 testing will have a separate test schedule and separate test suites, you must provide a separate device and attendee to conduct the FYI PCIe 4.0 testing for each product you register for the PCIe 4.0 testing.

PCI-SIG will also be scheduling PCIe 4.0 LEQ training and debug sessions with key tool providers Monday afternoon, for new registrants who need to validate the testability/operation of their PCIe 4.0 products ahead of scheduled testing. The test schedules cannot permit debugging during the actual test rotation and all products are expected to support PCIe 4.0 specification mandated loopback, link training, and loopback capabilities. If these capabilities are uncertain, notify the PCI-SIG (compliance@pcisig.com) at the time of registration, stating that Monday debug is requested. Electrical testing is still possible if the device operation can provide a non-standard method to enter the equivalent transmit compliance and loopback states.

PCI Express U.2/SFF-8639 (8 GT/s) Testing: Please note that as of Compliance Workshop #106 PCI-SIG is offering official SFF-8639/U.2 testing for both Add-in Cards and Systems. Testing can qualify a device for placement on the Integrators List. If you'd like your device listed as both CEM and U.2, on the Integrators List, please ensure that you register *TWO* instances of your device – one for the SFF-8639/ PCI Express U.2 testing, and one for the CEM form factor (bringing your own adapter for CEM).

SFF-8639/PCI Express U.2 Cards must be registered separately from CEM based Cards. Please note that one person cannot escort both SFF-8639/PCI Express U.2 Cards and CEM based cards, you will need to register an additional

attendee to manage the additional testing schedule. Please see the registration restrictions in the “How to Register” section.

PCI Express M.2 (8 GT/s) Preliminary FYI Testing: PCI-SIG is offering preliminary PCI Express M.2 testing for both Add-in Cards and Systems. Preliminary FYI Testing does **NOT** qualify a device for placement on the Integrators List. In order to perform both FYI testing and official testing for placement on the Integrators List, please ensure that you register **TWO** instances of your device – one for the M.2 testing, and one for the CEM form factor (bringing your own adapter for CEM).

M.2 Cards must be registered separately from CEM based and U.2 Cards. Please note that one person cannot escort M.2, SFF-8639/U.2 Cards or CEM based cards, you will need to register additional attendee(s) to manage the additional testing schedule(s). Please see the registration restrictions in the “How to Register” section.

Cost

Attendance at this **Members Only** event is free. Please note that your credit card information will be collected for product registration(s); however, you will **not** be charged unless you do not bring your product to the event or your product registration is not cancelled by **12noon on November 9, 2018**.

This event is non-hosted. Participants pay for their own travel, food, and hotel expenses. Lunch is provided for attendees Tuesday through Friday.

Where

Embassy Suites Burlingame – San Francisco Airport Hotel
150 Anza Blvd.
Burlingame, CA 94010

Shipping Instructions

Please ensure your shipment does not arrive at the hotel prior to **Thursday, November 27, 2018**. If you would like to ship items to the hotel, **please ensure you complete the [group package delivery form](#) and send to Vanessa Siu using the contact information provided on the form.**

Ship to: Embassy Suites Burlingame – San Francisco Airport Waterfront, 150 Anza Blvd., Burlingame, CA 94010 USA
Tel: 650-342-4600, Fax: 650-343-8137
Attn: Hold for (Your name / Your company name / Tel.) / Dec 4-7, 2018 PCI-SIG / K. McCann

Hotel Reservations – Reserve your room early!

Reservations cut-off date is **November 19, 2018.* To ensure the group rate is applied and the hotel has rooms available please make your reservations as soon as possible. Single Room \$199.00 + state and local taxes - (This is a special PCI-SIG Compliance Workshop rate. This rate includes breakfast provided by the hotel from 6:00 am – 9:00 am and Evening Reception from 5:30 pm – 7:30 pm daily) **Note: A one-time \$50 fee will be charged for any early guestroom departures.**

When making a hotel reservation, please use our group specific page at:

http://embassysuites.hilton.com/en/es/groups/personalized/S/SFOBGES-PCI-20181202/index.jhtml?WT.mc_id=POG

HOW TO REGISTER

I. EVENT REGISTRATION

- All Attendees **MUST** accompany a co-worker who is registered to test equipment.
- **On-Line Registration** – <https://pcisig.com/events/pci-sig-compliance-workshop-108>.
- Online registration ends at **12:00pm Pacific Time on Friday, November 9, 2018**.

II. SLEEPING ROOM RESERVATIONS

- Instructions are found on page 2
- **NOTE: A testing room must be reserved by the System vendor company to participate.**

III. ON-SITE CHECK-IN

- Name badges and test schedules will be distributed on Tuesday morning from 8:00-8:45am outside the PCI-SIG Hospitality Suite.

Schedule

IMPORTANT NOTE: Testing schedule is subject to change based on workshop size.

Monday, December 3, You DO NOT need to check-in on Monday, December 3, as name badges and test schedules will be distributed on Tuesday from 8:00am-8:45am outside the PCI-SIG Hospitality Suite.

Tuesday, December 4, will consist of 45-minute testing sessions that will be scheduled by PCI-SIG.

Tues. Dec. 4	8:00 AM – 8:45 AM	Schedule Distribution and Name Badge pick-up outside Hospitality Suite
	8:45 AM - 9:30 AM	Testing Session
	9:30 AM - 10:15 AM	Testing Session
	10:15 AM - 11:00 AM	Testing Session
	11:00 AM - 11:45 AM	Testing Session
	11:45 AM - 12:30 PM	Testing Session
	12:30 PM - 1:15 PM	Lunch
	1:15 PM - 2:00 PM	Testing Session
	2:00 PM - 2:45 PM	Testing Session
	2:45 PM - 3:30 PM	Testing Session
	3:30 PM - 4:15 PM	Testing Session
	4:15 PM - 5:00 PM	Testing Session
	5:00 PM - 5:45 PM	Testing Session
	5:45 PM - 6:30 PM	Testing Session

Wednesday, December 5 - Thursday, December 6 will consist of 45-minute testing sessions that will be scheduled by PCI-SIG.

Wed.-Thur. Dec. 5-6	8:00 AM - 8:45 AM	Testing Session
	8:45 AM - 9:30 AM	Testing Session
	9:30 AM - 10:15 AM	Testing Session
	10:15 AM - 11:00 AM	Testing Session
	11:00 AM - 11:45 AM	Testing Session
	11:45 AM - 12:30 PM	Testing Session
	12:30 PM - 1:15 PM	Lunch
	1:15 PM - 2:00 PM	Testing Session
	2:00 PM - 2:45 PM	Testing Session
	2:45 PM - 3:30 PM	Testing Session
	3:30 PM - 4:15 PM	Testing Session
	4:15 PM - 5:00 PM	Testing Session
	5:00 PM - 5:45 PM	Testing Session
	5:45 PM - 6:30 PM	Testing Session

Friday, December 7 will consist of 45-minute testing sessions that will be scheduled by PCI-SIG.

Fri. Dec. 7	8:00 AM - 8:45 AM	Testing Session
	8:45 AM - 9:30 AM	Testing Session
	9:30 AM - 10:15 AM	Testing Session
	10:15 AM - 11:00 AM	Testing Session
	11:00 AM - 11:45 AM	Testing Session
	11:45 AM - 12:30 PM	Testing Session
	12:30 PM - 1:15 PM	Lunch

PCI-SIG Compliance Workshop Test Syllabus

One-on-One Test Sessions

PCI-SIG Compliance Workshops include one-on-one private 45 minute test sessions between an Add-in Card vendor and a System vendor. System vendors and Add-in Card vendors will test their products in as many combinations as time and capabilities permit. The results of these test sessions are recorded and will be used as one of the elements in determining PCI-SIG compliance.

The one-on-one test sessions are held in hotel suites as reserved by the System/Systemboard/BIOS vendors (Add-in Card vendors travel to the system vendor suites). The privacy of these sessions promotes willingness to test products that may not be fully "market ready." Additionally, vendors are able to establish contacts which enable a strong PCI Engineering community and encourage future cooperation for debugging and development.

Test Details:

1. Install Add-in Card to be tested and attach peripheral (as appropriate).
2. Boot OS.
3. Run Application(s) to demonstrate detection & functional operation, file transfers for disk and LAN adapters, and graphics application for graphics adapters (note that demonstrating only detection and configuration is not adequate, as demonstrating functional operation is also required).

Add-In Card Test Sessions

PCI-SIG hosts staffed compliance test suites to test PCI Add-in Cards using test software and hardware to provide extensive testing for compliance against specific aspects of the appropriate PCI-SIG specification. Add-in Card vendors will be scheduled to visit each applicable compliance test suite during the compliance one-on-one test sessions. Results will be recorded and used as one of the elements in determining PCI-SIG compliance.

Test Details:

1. Remove (if necessary) system Add-in Card containing the same function that the Add-in Card being tested represents.
2. Insert vendor Add-in Card to be tested and attach the appropriate peripheral.
3. Load drivers (if needed) and the run tests.

System/Systemboard/BIOS Test Sessions

PCI-SIG hosts staffed compliance test suites for testing System/Systemboard/BIOS products. Commercially available PCI-SIG test cards and test software serve as the test platform for extensive testing for specific aspects of the system environment. System vendors will need to roam to the PCI-SIG Gold Suite system compliance test sessions.

Integrators List Policy

Products that pass 80% of one-on-one interoperability test sessions, pass all appropriate PCI-SIG compliance tests, **and** submit the [Product Listing Request Form](#), may be listed on a PCI-SIG Integrators List. The Integrators List is used by a number of PCI-SIG companies to make product purchase decisions. When submitting the [Product Listing Request Form](#), only one product will be listed per product tested at a PCI-SIG Compliance Workshop. A product may be submitted at any point following the workshop where the product was tested, there is no deadline.

Questions?

If you have any questions, please contact PCI-SIG at Tel. (503) 619-0569 or Fax (503) 644-6708 or e-mail PCI-SIG Administration at compliance@pcisig.com.

Thank you,

Reen Presnell
Executive Director
PCI-SIG