



PCI-SIG® Virtual Compliance Workshop #116

January 11 – 22, 2021
PCI-SIG Headquarters
3855 SW 153rd Drive
Beaverton, Oregon 97003

Please read carefully for registration information and the details.

When

The PCI-SIG Virtual Workshop #116 will be held January 11 – 22, 2021. The registration deadline is **5:00pm PT on Thursday, December 10, 2020.**

How

PCI-SIG will offer a Virtual Workshop for **Integrators List ready products only**. No onsite participants will be allowed to accompany Devices Under Test. Only **one** copy of the Device Under Test will be taken to each testing suite, separate devices for specific tests will not be allowed. Video conference calls will be established to allow members to view **Gold Suite test sessions only**. Testing times will not be adjusted if participants do not call in or if there are video conferencing difficulties at the time of the test.

As this workshop is intended for Integrators List ready product only, PCI-SIG offers no commitment for debugging. You will be able to witness Gold Suite tests as they occur, however there will be minimal communication for guidance purposes only. **No retests** will be guaranteed during the Virtual Workshop. Please note that if a failing issue is noted during Interoperability testing you will be contacted by the PCI-SIG by the end of the first week to schedule a call regarding the issue.

Final results will be provided the week after the Virtual Workshop. Preliminary checks will be possible during the week of the event, more details will be provided before testing begins regarding schedules, virtual logins and results tracking.

Cost

Participation for this **Members Only** event is free. Please note that your credit card information will be collected for product registration(s); however, you will **not** be charged unless your product does not arrive to the event by the shipping deadline of **Friday, December 18, 2021** or product registration is not cancelled by **5:00pm PT on Thursday, December 10, 2020.**

Where

PCI-SIG Headquarters
3855 SW 153rd Drive
Beaverton, OR 97003 USA

Shipping Instructions

Members will be responsible for shipping products for testing in both directions (cost and labels). Your product must arrive at PCI-SIG headquarters by **Friday, December 18, 2021.**

Please do not ship your product until PCI-SIG Administration (compliance@pcisig.com) has sent an email with specific instructions on product shipping. Once the minimum number of products have been registered to hold the event, a notice will be sent requesting for the product to be shipped.

Return shipping will occur the week following the workshop. Please provide a printed copy of the return shipment label in your initial shipment. For an example of a return shipment label, please see the photo [here](#). For FedEx and UPS return shipments, PCI-SIG will use its daily pickups. For any DHL shipments please set a pickup time for the week of January 22, 2021.

Ship to:

PCI-SIG
Attn: Kaitlyn Gooding

3855 SW 153rd Drive
Beaverton, OR 97003 USA

Objective

PCI: Compliance Workshops are held to promote PCI Express® specification compliance in the industry with the goals of eliminating interoperability issues and ensuring proper implementation of PCI specifications.

Member: Products may be added to the PCI-SIG Integrators List by passing the testing requirements at a compliance workshop **and** submitting a PCI-SIG Integrators List [Product Listing Request Form](#).

What Technologies Will Be Tested

PCI Express 4.0
PCI Express 3.0
PCI Express U.2®/SFF-8639/ (8 GT/s)

PCI Express 4.0 Official Testing: The PCI-SIG will be offering official testing for PCIe 4.0 at 16 GT/s for both add-in cards and systems.

- **Lane Margining:** please be aware that the Lane Margining test that was conducted at previous FYI workshops will remain an FYI test for 4.0 Integrators List testing.
- **Please note the following component policy for 4.0 official Integrators List testing:** PCI-SIG Integrators List testing for CEM devices is performed at the PCI Express connector, and AICs or System boards that pass can be added to the Integrators List. Vendors that wish to place the corresponding component onto the Integrators List based on that passing result, must certify to PCI-SIG that the electrical interface exposed at the CEM connector is the electrical interface of that component. Note that devices such as redrivers or retimers when present between the component and the CEM connector prevent the full electrical and functional testing of the component itself, and therefore while such an AIC or System board may achieve a passing result (and be placed on the Integrators List as an AIC or System), the component itself has not achieved a passing result and therefore cannot be placed on the Integrators List as a Component unless specifically tested.

PCI Express U.2/SFF-8639 (8 GT/s) Testing: Please note that as of Compliance Workshop #106 PCI-SIG is offering official SFF-8639/U.2 testing for both Add-in Cards and Systems. Testing can qualify a device for placement on the Integrators List. If you'd like your device listed as **both** CEM and U.2, on the Integrators List, please ensure that you register ***TWO*** instances of your device – one for the SFF-8639/ PCI Express U.2 testing, and one for the CEM form factor (bringing your own adapter for CEM).

SFF-8639/PCI Express U.2 Cards must be registered separately from CEM based Cards. Please note that one person cannot escort both SFF-8639/PCI Express U.2 Cards and CEM based cards, you will need to register an additional attendee to manage the additional testing schedule. Please see the registration restrictions in the “How to Register” section.

HOW TO REGISTER

I. EVENT REGISTRATION

- On-Line Registration – <https://pcisig.com/events/compliance-workshops>.
- Online registration ends at **5:00pm Pacific Time on Thursday, December 10, 2020.**

PCI-SIG Compliance Workshop Test Syllabus

Summary of Tests Performed

Refer to <https://members.pcisig.com/wg/PCI-SIG/document/8452> for the summary of test performed on your type and generation of device.

Integrators List Policy

Products that pass 80% of one-on-one interoperability test sessions, pass all appropriate PCI-SIG compliance tests, **and** submit the [Product Listing Request Form](#), may be listed on a PCI-SIG Integrators List. The Integrators List is used by a number of PCI-SIG companies to make product purchase decisions. When submitting the [Product Listing Request Form](#), only one product will be listed per product tested at a PCI-SIG Compliance Workshop. A product may be submitted at any point following the workshop where the product was tested, there is no deadline.

Questions?

If you have any questions, please contact PCI-SIG at Tel. (503) 619-0569 or Fax (503) 644-6708 or e-mail PCI-SIG Administration at compliance@pcisig.com.

Thank you,

Reen Presnell
Executive Director
PCI-SIG