

The PCIe 7.0 Specification, Version 0.3 is Now Available to Members

By Al Yanes, PCI-SIG President and Chairperson

PCI-SIG® is pleased to share the PCI Express® (PCIe®) 7.0 specification, version 0.3 is now available to members. Version 0.3 of the specification indicates that the first review draft of the specification is complete and has received work group approval. This is an important milestone for PCI-SIG, demonstrating we are on plan for a full specification release in 2025.

The PCIe 7.0 specification is targeted to support emerging applications such as 800 G Ethernet, AI/ML, Cloud and Quantum Computing; and data-intensive markets like Hyperscale Data Centers, High-Performance Computing (HPC), Edge and Military/Aerospace.

PCIe 7.0 Specification Feature Goals:

- Delivering 128 GT/s data rate and up to 512 GB/s bi-directionally via x16 configuration
- Utilizing PAM4 signaling
- Defining the channel parameters
- Continuing to deliver the low-latency and high-reliability targets
- Improving power efficiency
- Maintaining backwards compatibility with all previous generations of PCIe technology

Our robust and proven specification development process, spanning three decades, continues to guide us towards success. Thanks to the hard work and dedication of our work groups, PCI-SIG is ahead of the curve in providing the industry with a high-bandwidth, low-latency interconnect for emerging markets.

Join PCI-SIG and Contribute to PCIe Specification Developments

Our one-tier membership model means that every member company has the opportunity to contribute to the spec development process. Members can access the PCIe 7.0 specification, version 0.3 on the [members workspace](#).

You can view the complete list of our 940+ member companies [here](#). If you are not yet a PCI-SIG member, we invite you to [become a member](#) today.

Follow us on [LinkedIn](#) and [Twitter](#) for the latest information about the PCIe specifications and future announcements.