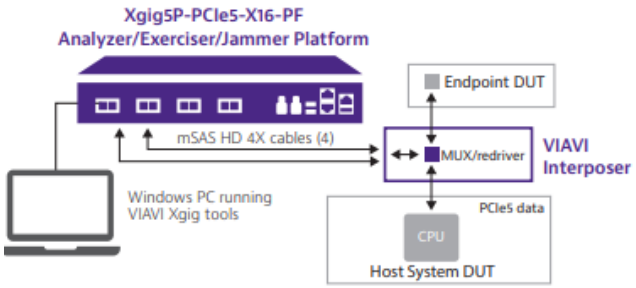
Product Flash

VIAVI announces Xgig® 5P16 Analyzer/Exerciser/Jammer platform for PCI Express® 5.0 Architecture

Single chassis supports all three functions: Analyzer/Exerciser/Jammer –and decodes all PCIe®, NVMe and CXL technology traffic at all layers of the stack.



The Xgig 5P16 allows for simultaneous protocol analysis for PCI Express® (PCIe®) 5.0 architecture traffic at all layers of the stack. This product supports link and transaction testing, analysis and advanced debug—at link speeds to 32 GTps and widths up-to 16-lanes.

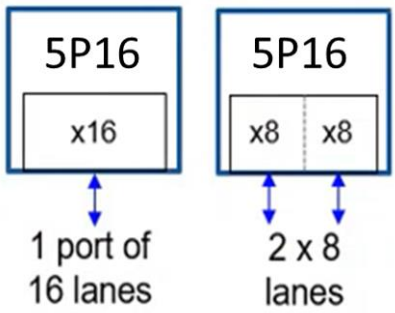
Multiple Functions on a Single Platform for Equipment Optimization

Unique to VIAVI, this platform is designed to integrate single or multiple Analyzer, Exerciser and Jammer functions on the same chassis. This approach provides users with the flexibility to optimize equipment to best meet operational and budgetary requirements:

- **Save Time—**Engineers and technicians can execute exerciser, analyzer and jammer functions simultaneously *or* separately on the same hardware, so equipment does not have to be disconnected and replaced with a different device to perform a different test.
- **Save Space—**The VIAVI Xgig platform enables users to configure the hardware for single or multiple functions as needed and available space dictate.
- **Cut Costs—**The combined functionality on one chassis provides cost flexibility that single function devices simply cannot provide. For example, users can buy an Xgig Exerciser/Analyzer today, and add Jammer functionality when it is needed.

Bifurcation Support Provides Additional Operational and Cost Efficiencies

The Xgig 5P16 platform has also been upgraded to support bifurcation capabilities that enable multiple users and simultaneous tests on the same platform. PCIe link bifurcation works by dividing a port that is normally one link, into two or more links. This, in effect, creates multiple independent analyzers, one for each new link, and thereby eliminates the need for a second analyzer chassis in some cases. The ability to bifurcate a wide 16-lane link into multiple smaller links creates a large set of possible test configurations. This provides additional operational and cost efficiencies for better return on investment—and usage flexibility enabling quicker analysis and debugging at a lower total cost.



Simple bifurcation example dividing a 1-port, 16-lane analyzer into two independent ports of 8-lanes each.

"As a supporter of early developers of the PCIe 5.0 architecture, VIAVI plays an important role for PCI-SIG® in the pursuit of adoption of PCIe 5.0 technology," said Al Yanes, PCI-SIG Chairman and President. "Products such as this are an indicator of industry support for this new technology, giving users a new tool to help validate their PCIe 5.0 technology implementations."

For more information about the VIAVI Xgig 5P16, please watch the video, visit our website, or contact your local VIAVI representative.

About PCI-SIG

PCI-SIG® is the consortium that owns and manages PCI specifications as open industry standards. The organization defines industry standard I/O (input/output) specifications consistent with the needs of its members. Currently, PCI-SIG is comprised of over 900 industry-leading member companies. To join PCI-SIG, and for a list of the Board of Directors, visit www.pcisig.com.

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