



PRESS RELEASE

## Pericom Expands Support for High Speed Connectivity

*Newest Products Benefit Networking, Embedded, Server, and Mobility Applications*

**INTEL DEVELOPER FORUM (IDF), San Francisco, CA, August 16, 2016** – Pericom Semiconductor Corporation (now a part of Diodes Incorporated, NASDAQ: DIOD), a leading supplier of serial high-speed switching, connectivity, signal-integrity and timing solutions, today announced expanded product families in support of next generation networking, embedded, mobility, and server platforms. Pericom will display these products at IDF, Moscone Center, San Francisco, CA, August 16-18, 2016 at Booth # 620.

Networking control plane processors and embedded processor applications often need PCI Express® (PCIe®) port expansion technology to handle the newest and most complex data plane and embedded multi-function designs. Pericom has added four new packet switches based on the PCIe 2.0 specification, ranging from 8 port/8 lanes to 16 port/24 lanes. These latest additions provide advanced power saving and management, DMA (Direct Memory Access), and are optimized to provide cost effective port expansion across a wide range of networking and embedded applications.

“As a PCI-SIG® member, Pericom holds an important role in the success of PCIe technology,” said Al Yanes, PCI-SIG chairman and president. “The company’s support of PCIe solutions helps the PCI Express ecosystem attain applicability within an increasing range of platforms in the market.”

Many of the latest server and storage platforms utilize multiple high speed protocols, such as 10Gbps USB 3.1 and PCIe 3.0 technology at 8Gbps, 10GbE, and 12Gbps SAS3. Pericom has added two multi-protocol linear redriver ICs to its signal integrity solutions family that can handle all 3 protocols and meet link training and standards compliance specifications from 8Gbps to 12Gbps. The new parts offer flow through pinout, low power, and 2 or 8 fully independent user programmable channels of signal boost to extend high speed traces or cables in server and storage platforms.

Pericom is also the first to offer a fully linear 2 channel USB 3.1 redriver that supports both 10Gbps (USB 3.1 GEN2) and 5Gbps (USB 3.1 GEN1) speeds.

Additional new switch products for server and storage platforms include two I<sup>2</sup>C multiplexers (MUX) that offer 1:4 and 1:8 fan-out capability of the I<sup>2</sup>C bus, with any combination of downstream ports selectable. These parts add flexibility to I<sup>2</sup>C bus designs in more complex platforms where the I<sup>2</sup>C bus is also tasked to provide I/O functionality as well as legacy I<sup>2</sup>C control. A new 14bit SSD switch is ONFI3 compliant, optimized for supporting high capacity SSD memory modules through fast enable and select timing.

Moreover, five new PCIe 3.0 very low power clock generators and buffers offering user programmable slew rate and supporting x2/x4/x8 outputs, expand the timing product portfolio for server, storage, and embedded platforms.

“Pericom’s new product roadmap continues to benefit our processor chipset partners,” said Andy Tsong, division manager for Pericom products. “We enable and maximize the new features that are used by platform designers. For example, PCI Express technology continues to demand new timing, switching, conditioning, and routing solutions, and Pericom’s focus and roadmaps support all of these necessary functions.”

**Product Part Numbers and Availability:**

Samples and production quantities for most products are available now.

**PCIe 2.0 Packet Switch products:**

- [PI7C9X2G808PR](#) - 8-port, 8-lane, PCIe 2.0 Packet Switch
- [PI7C9X2G912GP](#) - 9-port, 12-lane, PCIe 2.0 Packet Switch
- [PI7C9X2G1616PR](#) - 16-port, 16-lane, PCIe 2.0 Packet Switch
- [PI7C9X2G1224GP](#) - 12-port, 24-lane, PCIe 2.0 Packet Switch

**Switching products:**

- [PI4MSD5V9546A](#) - 4-Channel I<sup>2</sup>C /SMBus Bus Switch with RESET
- [PI4MSD5V9548A](#) - 8-Channel I<sup>2</sup>C /SMBus Bus Switch with RESET
- [PI2SSD3212](#) - 1.35V/ 1.5V/1.8V 14 bit 2:1 SSD Switch

**PCIe 3.0 1.8V Low Power HCSL Timing products:**

- [PI6CG18201](#) - Clock Generator with 2 outputs and on-chip termination
- [PI6CG18401](#) - Clock Generator with 4 outputs and on-chip termination
- [PI6CG18801](#) - Clock Generator with 8 outputs and on-chip termination
- [PI6CB18800](#) - Clock Buffer with 8 outputs
- [PI6CB18801](#) - Clock Buffer with 8 outputs with on-chip termination

**Recently announced USB 3.1, PCIe 3.0/10GbE/SAS3 Combo ReDriver/Repeater products:**

- [PI3EQX12908A](#) - 8-Channel PCIe 3.0/10GbE/SAS3/SATA3 Combo Linear ReDriver
- [PI3EQX12902B](#) - 2-Channel PCIe 3.0/10GbE/SAS3/SATA3 Combo Linear ReDriver
- [PI3EQX1002B](#) - USB 3.1 Gen 2 Linear ReDriver / Repeater

Additional information and a volume pricing quote is available from Pericom [worldwide sales](#), or by [requesting](#) a specific product price quote on [www.pericom.com](#).

**About Pericom:**

Pericom Semiconductor Corporation enables serial connectivity with complete solutions for computing, communications and consumer market segments. Pericom’s integrated circuits (IC) and frequency control products (FCP) provide the connectivity, timing, switching, bridging and signal conditioning of high-speed signals required by today’s electronic applications. Pericom is based in Milpitas, California, with design centers and technical sales and support offices around the world. Pericom was acquired by [Diodes Incorporated](#) (NASDAQ: DIOD). Learn more at [www.pericom.com](#) / [www.diodes.com](#).

**About PCI-SIG**

PCI-SIG is the consortium that owns and manages PCI™ specifications as open industry standards. The organization defines industry standard I/O (input/output) specifications consistent with the needs of its members. Currently, PCI-SIG is comprised of nearly 800 industry-leading member companies. To join PCI-SIG, and for a list of the Board of Directors, visit [www.pcisig.com](http://www.pcisig.com).

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